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First Named Inventor	Chistophe Chevallier	
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Title: FLASH ARRAY	IMPLEMENTATION WITH	LOCAL AND GLOBAL BIT LINES
		Sheet 1 of 1

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VIET Q. NGUYEN PRIMARY EXAMINER

Examiner Signature	V-Nonejan	Date Considered	4/11/04	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not				

in conformance and not considered. Include copy of this form with next communication to applicant.

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